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CONFIRMATION NO. 8467

Bib Data Sheet

|                             |                                       |              |                        |  |
|-----------------------------|---------------------------------------|--------------|------------------------|--|
| SERIAL NUMBER<br>10/620,509 | FILING DATE<br>07/16/2003<br><br>RULE | CLASS<br>327 | GROUP ART UNIT<br>2816 | ATTORNEY DOCKET<br>NO.<br>14815-015001 |
|-----------------------------|---------------------------------------|--------------|------------------------|--|

## APPLICANTS

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\*\* CONTINUING DATA *None*
 \*\* FOREIGN APPLICATIONS *Yes*  
 JAPAN P. 2002-210396 07/19/2002

 IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 10/15/2003

|   |  |                               |                      |                            |
|---|--|-------------------------------|----------------------|----------------------------|
| Foreign Priority claimed<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no   | STATE OR<br>COUNTRY<br>JAPAN               | SHEETS<br>DRAWING<br>5        | TOTAL<br>CLAIMS<br>5 | INDEPENDENT<br>CLAIMS<br>3 |
| 35 USC 119 (a-d) conditions met<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | EXAMINER'S SIGNATURE<br><i>[Signature]</i> | INITIALS<br><i>[Initials]</i> |                      |                            |
| Verified and Acknowledged   |  |                               |                      |                            |

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## TITLE

Phase locked loop circuit using fractional frequency divider

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|------------------------------------|---|---|
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